

FIG. 2

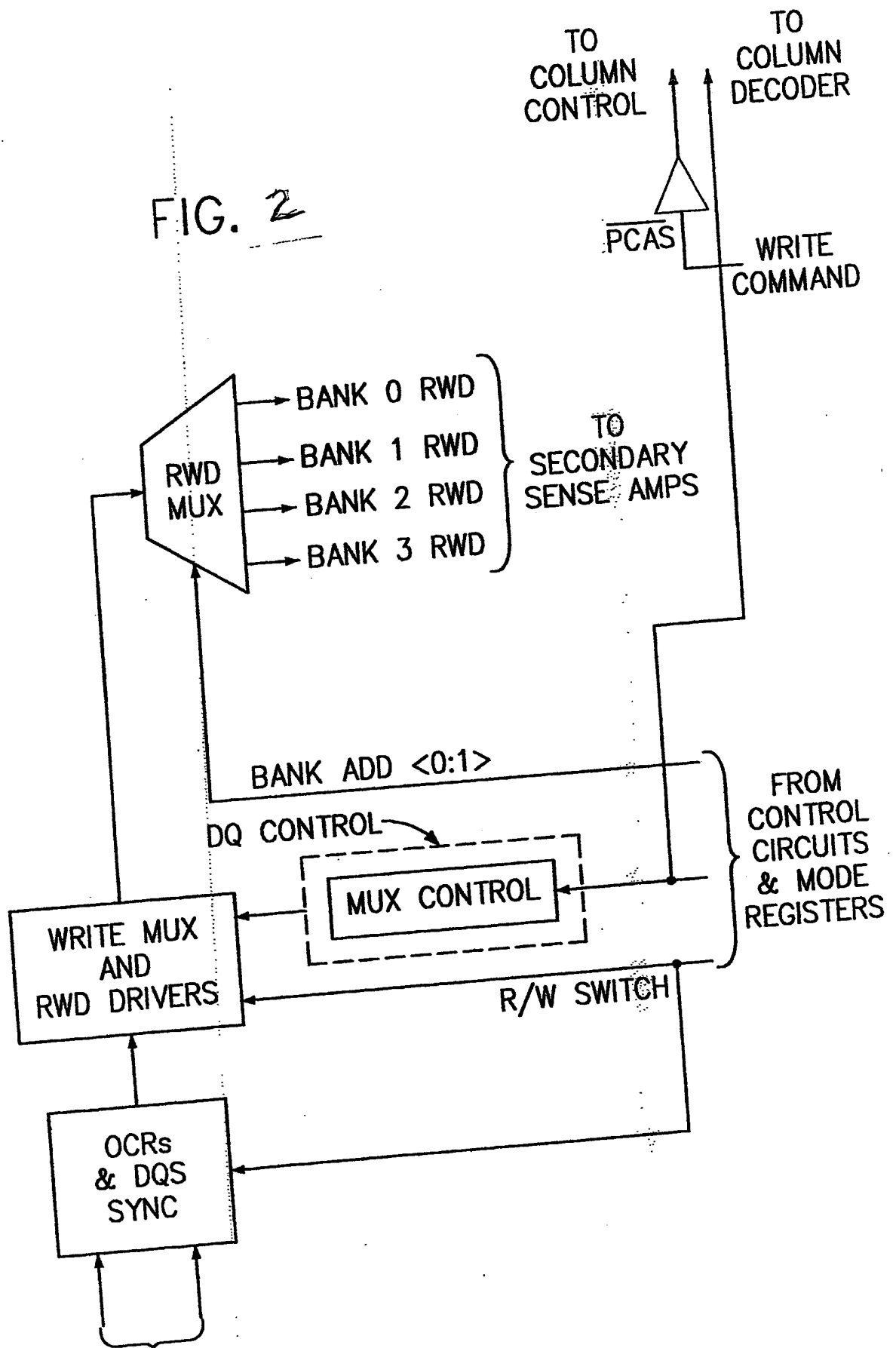
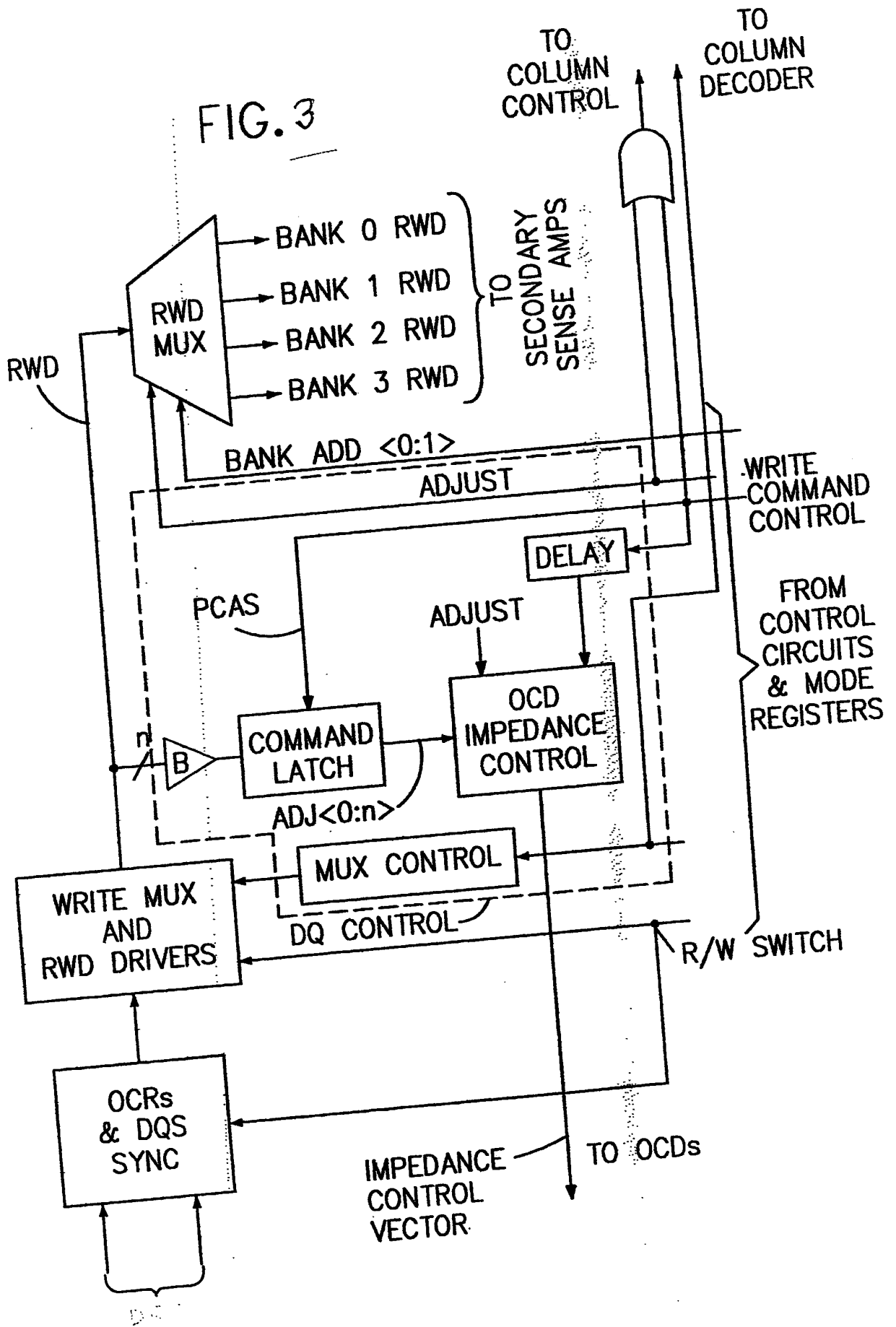


FIG. 3



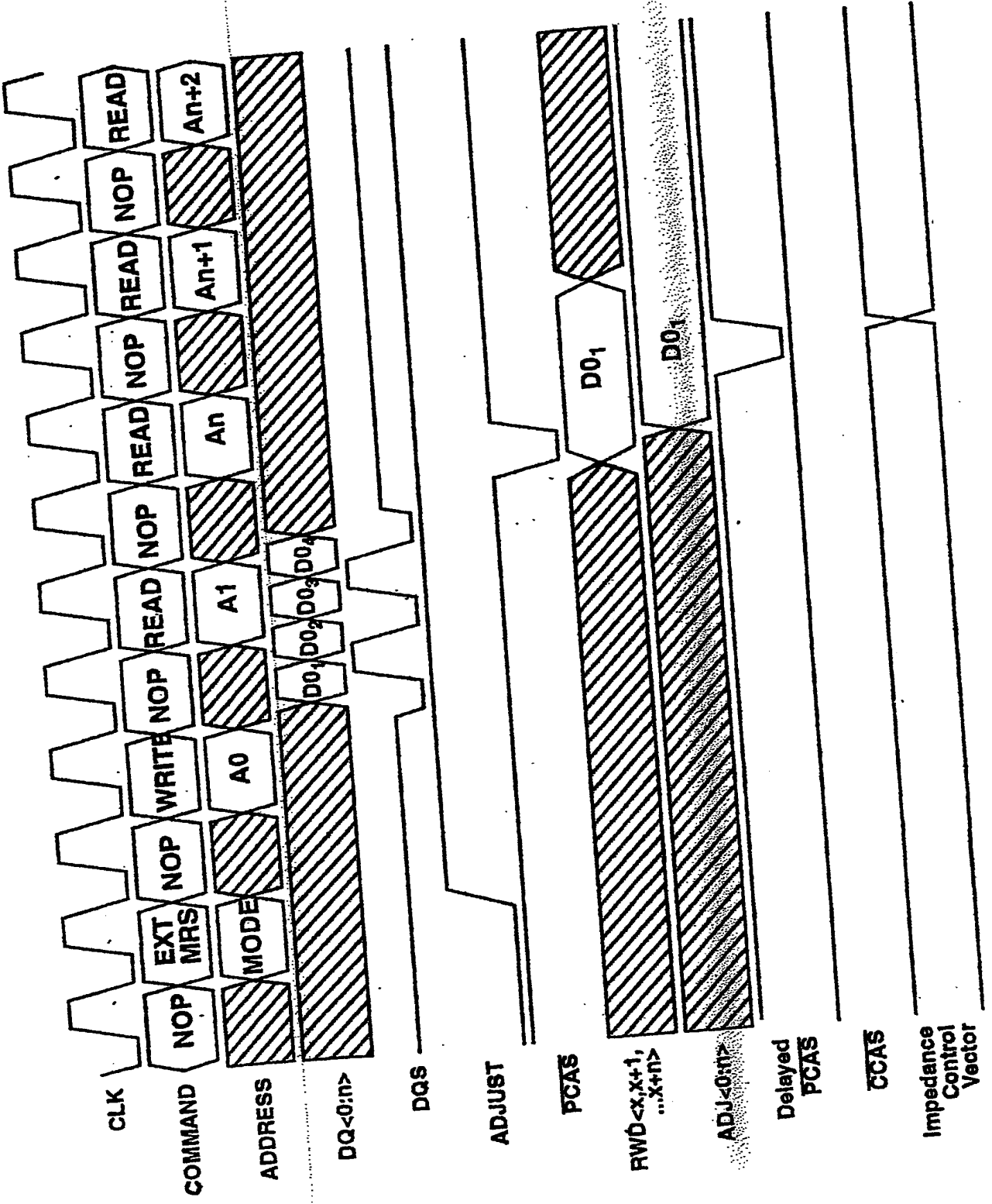
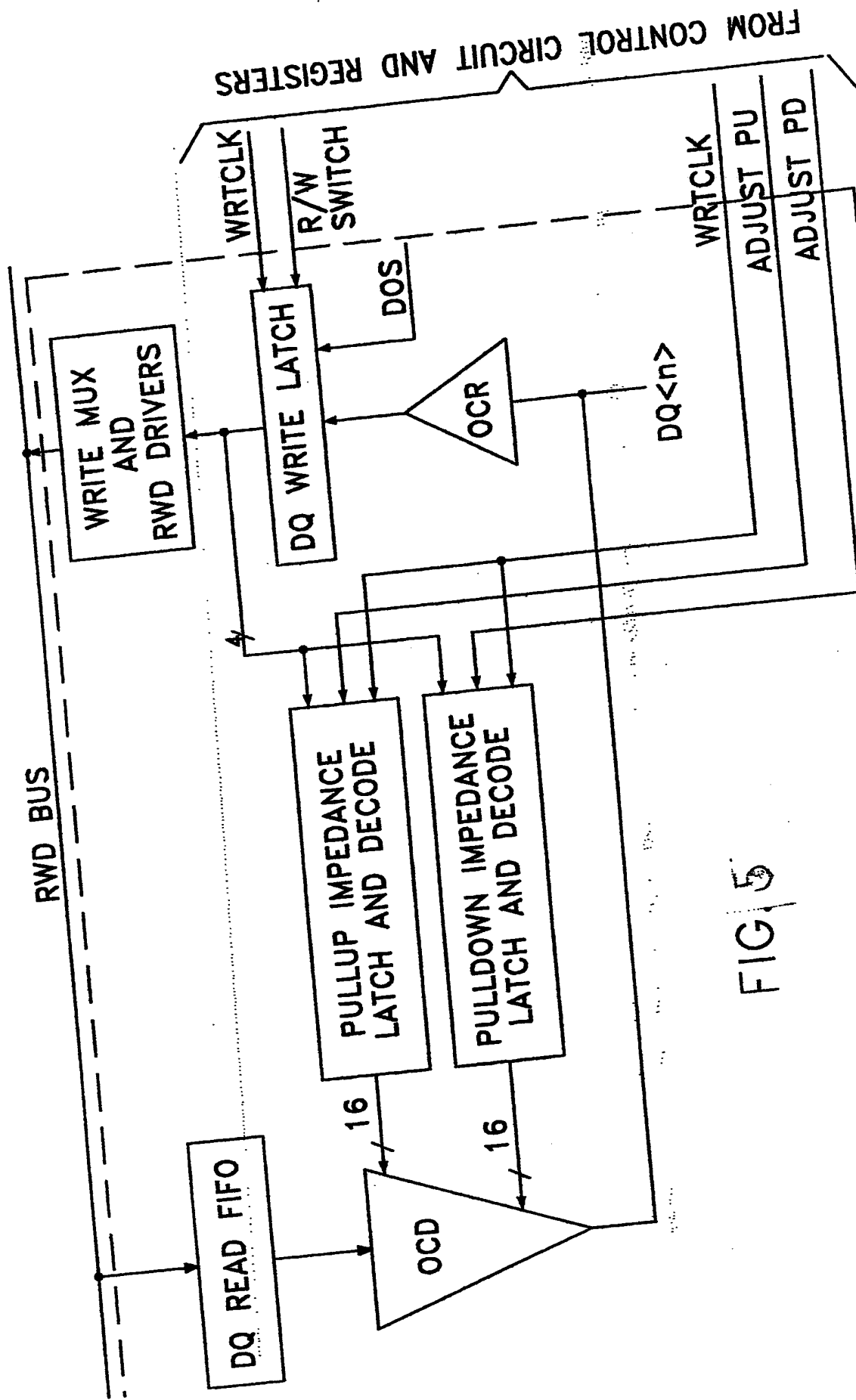


FIG. 4



FROM CONTROL CIRCUIT AND REGISTERS

FIG. 5

1 DQ CIIN